

DOCUMENT-IDENTIFIER: US 5796267 A

TITLE: Tri-Statable input/output circuitry for programmable logic

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BSPR:

If desired, the OE signal for each pin may be additionally programmably selectable from other sources such as (1) one or more "global" output enable signals available on the programmable logic device, (2) a source of fixed logic 1 potential (e.g., the source of VCC potential), and/or (3) a source of logic 0 potential (e.g., the source of VSS (ground) potential).

Also if

desired, the output signal for each pin may be additionally programmably selectable from other sources such as (1) a source of fixed logic 1 potential (e.g., VCC) and/or (2) a source of fixed logic 0 potential (e.g., VSS).

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TITLE: Integrated circuit with bidirectional pin

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BSPR:

For example, consider FIG. 1, which shows an integrated circuit having a first digital output pin A where a voltage value may be present which corresponds to either a logic low (0-2.5 volts) or a logic high (3.5-5 volts). A second pin B is desired for digital or control input, and its state may either be, for example, an open circuit or of grounded connection. It is apparent that if pins A and B coincided, a control signal received on pin B would interfere with the proper operation of the output at pin A.